



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al.

Art Unit : 2183

Serial No. : 10/070,092

Examiner : Kevin P. Rizzuto

Filed : June 28, 2002

Title : LOCAL REGISTER INSTRUCTION FOR MICRO ENGINE USED IN
MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE**MAIL STOP RCE**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants disclose the documents listed on the attached PTO-1449 form. Under 37 CFR § 1.98 (a)(2)(ii), only copies of foreign patent documents and non-patent literature are enclosed. Copies of any listed U.S. patents can be provided upon request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: Sept. 7, 2005

Ido Rabinovitch
Reg. No. L0080

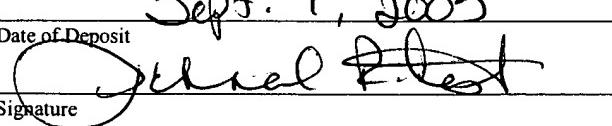
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225 Franklin Street
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Telephone: (617) 542-5070
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-309US1	Application No. 10/070,092
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date June 28, 2002	Group Art Unit 2183

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	4,992,934	02-1991	Portanova et al.			
	AB	5,450,603	09-1995	Davies			
	AC	5,870,597	02-1999	Panwar et al.			
	AD	6,009,505	12-1999	Thayer et al.			
	AE	6,408,325	06-2002	Shaylor			
	AF	2002/0056037	05-2002	Wolrich et al.			
	AG						
	AH						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AI	WO 01/16697	03-2001	WIPO				
	AJ							
	AK							
	AL							
	AM							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AN	Hennessey et al. "Computer Organization and Design: The Hardware/Software Interface." San Francisco, CA. Morgan Kaufmann Publishers, Inc. pp. 116-119, 181-182, 225-227, 466-470, and 510-517, 1996.
	AO	Heuring et al. "Computer Systems Design and Architecture." Reading, MA. Addison Wesley Longman, Inc., pp. 38-40, 143-171, and 285-288, 1997.
	AP	Probst et al. "Programming, Compiling and Executing Partially-Ordered Instruction Streams on Scalable Shared-Memory Multiprocessors." Proceedings of the 27 th Annual Hawaiian International Conference on System Sciences, IEEE, pp. 584-593, 1994.
	AQ	Chen et al. "The Compiler for Supporting Multireading in Cyclic Register Windows." IEEE, pp. 57-62, 1996.
	AR	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	